

PACET 2019

Panhellenic Conference on Electronics & Telecommunications

November 8-9, 2019, Volos, Greece

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Aims and Scopes:

Electronics and Telecommunications are two fundamental and rapidly evolving technologies, which constitute basic pillars of scientific and economic development. Given their important role and contribution from a technical, but even social, perspective, the 5th Panhellenic Conference on Electronics and Telecommunications-PACET, which is hosted by the Department of Electrical and Computer Engineering in Volos, aims at reporting progress and recent advances in all aspects of electronics and communications both at academic and industrial level. The topics include, but are not limited to:

- Analog/mixed-signal circuits
- Biomedical circuits and systems
- CAD and design tools
- Communication circuits and systems
- Computer architecture and memories
- Digital circuits
- Digital signal processing
- Embedded systems
- Energy harvesting circuits
- Imaging and image sensors
- Low-power low-voltage designs
- Power Electronics
- RF and wireless circuits and systems
- Sensory circuits and systems
- Telecommunications
- Test and verification

Important Dates:

Special Session Proposal: 13 May
Full Length Papers: 13 May 2019 - 1 August
PhD Forum Submission: 30 September
Notification of Acceptance: 10 September
Camera Ready Paper Submission: 04 October

Submission Guidelines:

Authors are encouraged to submit their manuscripts via EasyChair web service at web page:

<http://pacet2019.e-ce.uth.gr>

Each manuscript should include the complete paper text, all illustrations, and references. The manuscript should conform to the IEEE format: single-spaced, double column, US letter page size, 10-point size Times Roman font, up to 6 pages. In order to conduct a blind review, no indication of the authors' names should appear in the manuscript, references included. CPS, Conference Publishing Services, publishes the (ISI indexed) PACET Proceedings, available worldwide through the IEEE Xplore Digital Library.

For more information please contact:

University of Thessaly, Electrical and Computer Engineering Dept.
37 Glavani Str. Deligiorgi Building, 4th floor 382 21
Volos – Greece

Phone: +30 24210 74392

e-mail: fplessas@e-ce.uth.gr, georges@e-ce.uth.gr

Official Website: pacet2019.e-ce.uth.gr